JUN 2 1 2004

Page No.: 1 of: 1

INFORMATION DISCLOSURE CITATION FORM FOR PATENT APPLICATION (FORM PTO-1449) (Substitute)

Docket No.: YOR920030196US1

Serial No.: 10/700,327

Applicant(s): Paul S. ANDRY et al.

	(FORM P10-14	49)	1				
(Substitute)			Filing Date: 11/03/03	Group:	Group:		
			U.S. PATENTS				
Initials	Patent Number	Issue Date	Name	Class	Sub- class	Filing dat	
	5,244,143	09/14/93	Ference et al.				
	5,775,569	07/07/98	Berger et al.		i		
	5,998,292	12/07/99	Black et al.				
	6,506,332	01/14/03	Pedigo				
	6,593,644	07/15/03	Chiu et al.				
		FOI	L REIGN PATENT DOCUM	IENTS			
Initials	Document Number	Date	Country	Name	Name Translation Yes/No/r		
-							
						;	
				ļ			
	ОТНЕ	R DOCUME	NTS (Title, Author, Date,	, Pages, Etc., if know	1)		
	"Filling the via hole of IC by VPES (Vacuum Printing Encapsulation Systems for stacked chip (3D Packaging), A. Okuno et al., 2002 Electronics Components and						
	Technology Confe	erence					
						· · · · · · · · · · · · · · · · · · ·	
						-	
	er's Signature:			Date Considered			